



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

WARREN M. FARNWORTH
ALAN G. WOOD
DAVID R. HEMBREE

SERIAL NO.: 10/035,355

ART UNIT: 2827

FILING DATE: January 4, 2002

EXAMINER: THAI L.

For: METHOD FOR FABRICATING SEMICONDUCTOR
COMPONENTS AND INTERCONNECTS WITH
CONTACTS ON OPPOSING SIDES

Attorney Docket No.: 97-1363.3

#4/Election
RT
1/10/03

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RESPONSE
December 11, 2002

Assistant Commissioner of Patents
BOX AMENDMENT (NON FEE)
Washington, D.C. 20231

Sir:

This is a response to the Office Action dated December 3, 2002 having a statutory period for response set to expire on January 3, 2003.

REMARKS

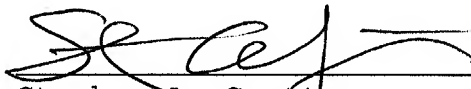
With respect to the restriction requirement contained in the Office Action, Applicants hereby elect ~~Invention II~~. Claims 1-31 read on the elected Invention II. Favorable consideration and allowance of claims 1-31 is respectfully requested.

An Information Disclosure Statement is also being filed with this Response. Should any issues arise that

will advance this case to allowance, the Examiner is asked to contact the undersigned by telephone.

DATED this 11th day of December, 2002.

Respectfully submitted:



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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Commissioner of Patents and Trademarks, BOX AMENDMENT (NON FEE), Washington, D.C. 20231 on this 11th day of December, 2002.

December 11, 2002

Date of Signature



Stephen A. Gratton
Attorney for Applicants